The Intel iSBX 354 Serial I/O MULTIMODULE board is a member of Intel's line of iSBX compatible MULTIMODULE products. The iSBX MULTIMODULE board plugs directly into any iSBX bus compatible host board offering incremental on-board I/O expansion. Utilizing Intel's 82530 Serial Communications Controller component, the iSBX 354 module provides two RS232C or RS422A/449 programmable synchronous/asynchronous communications channels. The 82530 component provides two independent full duplex serial channels, on chip crystal oscillator, baud-rate generator and digital phase locked loop capability for each channel. The iSBX board connects to the host board through the iSBX bus. This offers maximum on-board performance and frees the MULTIBUS® System bus for use by other system resources.
FUNCTIONAL DESCRIPTION

Communications Interface

The iSBX 354 module uses the Intel 82530 Serial Communications Controller (SCC) component providing two independent full duplex serial channels. The 82530 is a multi-protocol data communications peripheral designed to interface high speed communications lines using Asynchronous, Byte-Synchronous and Bit-Synchronous protocols to Intel's microprocessor based board and system level products. The mode of operation (i.e. asynchronous or synchronous), data format, control character format, and baud-rate generation are all under program control. The 82530 SCC component can generate and check CRC codes in any Synchronous mode and can be programmed to check data integrity in various modes. The command lines, serial data lines, and signal ground lines are brought out to a double edge connector.

The iSBX 354 module provides a low cost means to add two serial channels to iSBC® boards with 8 or 16 bit MULTIMODULE interfaces. In the factory default configuration, the iSBX 354 module will support two RS232C interfaces. With user supplied drivers and termination resistors, the iSBX 354 module can be reconfigured to support RS422A/449 communication interfaces with support on Channel A only for multidrop control from the base board. Both channels can be configured as DTE or DCE with RS232C interfaces.

Interrupt Request Line

The 82530 SCC component provides one interrupt to the MINTRO signal of the iSBX interface. There are six sources of interrupts in the SCC component (Transmit, Receive and External/Status interrupts in both channels). Each type of interrupt is enabled under program control with Channel A having higher priority than Channel B, and with Receive, Transmit

Figure 1. Installation of 2 iSBX™ 354 MULTIMODULE™ Boards on an ISBC® Board

Figure 2. Mounting Technique
and External/Status interrupts prioritized in that order within each channel.

Installation

The iSBX 354 module plugs directly into the female iSBX connector on the host board. The module is then secured at one additional point with nylon hardware to insure the mechanical security of the assembly. Figures 1 and 2 demonstrate the installation of the iSBX 354 MULTIMODULE board on a Host Board. Figures 3 and 4 provide cabling diagrams.

Programming Considerations

The Intel 82530 SCC component contains several registers that must be programmed to initialize and control the two channels. Intel's iSBX 354 Module Hardware Reference Manual (Order #146531-001) describes these registers in detail.

RS232C DB-25 CONNECTORS

![RS232C Cable Construction](image1)

Figure 3. RS232C Cable Construction

RS422A/449 DB-37 CONNECTORS

![RS422A/449 Cable Construction](image2)

Figure 4. RS422A/449 Cable Construction
SPECIFICATIONS

Word Size
Data—8 bits

Clock Frequency
4.9152 MHz

Serial Communications
Synchronous—Internal or external character synchronization on one or two synchronous characters
Asynchronous—5–8 bits and 1, 1½ or 2 stop bits per character; programmable clock factor; break detection and generation; parity, overrun, and framing error detection

Sample Baud Rate:

<table>
<thead>
<tr>
<th>Synchronous X1 Clock</th>
<th>82530 Count Value (Decimal)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Baud Rate</td>
<td></td>
</tr>
<tr>
<td>64000</td>
<td>36</td>
</tr>
<tr>
<td>48000</td>
<td>49</td>
</tr>
<tr>
<td>19200</td>
<td>126</td>
</tr>
<tr>
<td>96000</td>
<td>254</td>
</tr>
<tr>
<td>48000</td>
<td>510</td>
</tr>
<tr>
<td>2400</td>
<td>1022</td>
</tr>
<tr>
<td>1800</td>
<td>1363</td>
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<tr>
<td>1200</td>
<td>2046</td>
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<tr>
<td>300</td>
<td>8190</td>
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Asynchronous X16 Clock

<table>
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<tr>
<th>Baud Rate</th>
<th>82530 Count Value (Decimal)</th>
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<tbody>
<tr>
<td>19200</td>
<td>6</td>
</tr>
<tr>
<td>96000</td>
<td>14</td>
</tr>
<tr>
<td>48000</td>
<td>30</td>
</tr>
<tr>
<td>2400</td>
<td>62</td>
</tr>
<tr>
<td>1800</td>
<td>83</td>
</tr>
<tr>
<td>1200</td>
<td>126</td>
</tr>
<tr>
<td>300</td>
<td>510</td>
</tr>
<tr>
<td>110</td>
<td>1394</td>
</tr>
</tbody>
</table>

Signals Provided

RS232C DTE
- Transmit Data
- Receive Data
- Request to Send
- Clear to Send
- Data Set Ready
- Signal Ground
- Carrier Detect
- Transmit Clock (2)
- Receive Clock
- Data Terminal Ready
- Ring Indicator

RS232C DCE
- Transmit Data
- Receive Data
- Clear to Send
- Data Set Ready
- Signal Ground
- Carrier Detect
- Transmit Clock (2)
- Receive Clock
- Ring Indicator

RS422A/449
- Send Data
- Receive Timing
- Receive Data
- Terminal Timing
- Receive Common

I/O Port Addresses

<table>
<thead>
<tr>
<th>Port Address</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>X0</td>
<td>Read Status Channel B</td>
</tr>
<tr>
<td></td>
<td>Write Command Channel B</td>
</tr>
<tr>
<td>X2</td>
<td>Read Data Channel B</td>
</tr>
<tr>
<td></td>
<td>Write Data Channel B</td>
</tr>
<tr>
<td>X4</td>
<td>Read Status Channel A</td>
</tr>
<tr>
<td></td>
<td>Write Command Channel A</td>
</tr>
<tr>
<td>X6</td>
<td>Read Data Channel A</td>
</tr>
<tr>
<td></td>
<td>Write Data Channel A</td>
</tr>
<tr>
<td>Y0</td>
<td>Read Disable RS422A/449 Buffer</td>
</tr>
<tr>
<td></td>
<td>Write Enable RS422A/449 Buffer</td>
</tr>
</tbody>
</table>

NOTES:
1. The "X" and "Y" values depend on the address of the iSBX interface as viewed by the base board.
2. "X" corresponds with Activation of the MCS0/interface signal; "Y" corresponds with Activation of the MCS1/interface signal.

Power Requirements

+ 5V at 0.5A
+ 12V at 50 mA
− 12V at 50 mA

Physical Characteristics

Width: 2.85 inches
Length: 3.70 inches
Height: 0.8 inches
Weight: 85 grams
ENVIRONMENTAL CHARACTERISTICS
Temperature: 0°C to 55°C operating at 200 linear feet per minute across baseboard and MULTIMODULE board
Humidity: To 90%, without condensation

ORDERING INFORMATION
Part Number Description
iSBX 354 Dual Channel I/O MULTIMODULE

REFERENCE MANUAL
146531-001—iSBX 354 Channel Serial I/O Board Hardware Reference Manual
Reference manuals may be ordered from any Intel sales representative, distributor office, or from Intel Literature Department, 3065 Bowers Avenue, Santa Clara, CA 95051.